Application No.: Not Yet Assigned Docket No.: A8319.0015/P015-A

AMENDMENTS TO CLAIMS

1-6. (canceled)

7. (currently amended) A battery apparatus, comprising:

plural battery modules connected in series each having plural battery cells connected in series;

plural low order control devices which are provided in correspondence with the plural battery modules, respectively, each of the plural low order control devices controlling the plural battery cells configuring corresponding one of the plural battery modules;

a high order control device which controls the plural low order control devices;

a voltage detecting unit which detects voltages of the plural battery cells within the battery module; and

error ~~calibration terminal~~ compensation means which [[calibrates]] compensates an error of a voltage detected by the voltage detecting unit.

8. (currently amended) A battery apparatus according to claim 7, ~~wherein: the voltage detecting unit is~~ further comprising an A/D converter which converts a voltage detected by the voltage detecting unit into a digital value from an analog value and outputs the digital value; and

wherein the error compensation means has a terminal and is provided within the A/D converter; and

the low order control device ~~compensates an output value of the A/D converter by previously giving~~ applies a digital value to the ~~error calibration~~ terminal

in advance ~~of the A/D converter~~ thereby to compensate an output value of the A/D converter.

9. (currently amended) A battery apparatus according to claim 8, wherein:

the A/D converter comprises:

an integration unit which integrates a unit amount of electricity according to a number of pulses;

a comparing unit which compares an integral value of the integration unit with a voltage of the battery cell and stops the pulse;

a counter unit which outputs the number of pulses when the pulse is stopped by the comparing unit; and

wherein ~~a compensation unit which~~ the error compensation means compensates an output of the counter unit according to the digital value given to the ~~error calibration~~ terminal.

10. (currently amended) A battery apparatus according to claim 9, wherein the ~~compensation unit~~ error compensation means changes a counted value of the counter unit according to the digital value given to the ~~error calibration~~ terminal to compensate an offset of the A/D conversion and changes a width of the pulse to compensate a gain of the A/D conversion.